

**PATENT APPLICATION**  
**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

In re application of

Docket No: Q85504

Heiji WATANABE , et al.

Application No.: 10/519,084

Group Art Unit: 2822

Confirmation No.: 7332

Examiner: Tsz K. CHIU

Filed: December 23, 2004

For: SEMICONDUCTOR DEVICE AND ITS MANUFACTURING METHOD

**RESPONSE UNDER 37 C.F.R. § 1.111**

**MAIL STOP AMENDMENT**

Commissioner for Patents

P.O. Box 1450

Alexandria, VA 22313-1450

Sir:

In response to the Office Action dated October 1, 2008, please consider Applicants' remarks regarding the above-identified application as follows on the accompanying pages.

**TABLE OF CONTENTS**

REMARKS.....	2
--------------	---